



TR031 Customizing the SDLC for Your Organization

Individual Student Cost: **\$575**

Enroll in 3 IACP classes and get \$300.00 credit for the 4th!
Training Coordinators: Enroll 4 students and the 5th is free!

Duration: 3 days

Audience:

This course is designed for experienced business and technical experts, managers, project leaders, and JAD participants that want to learn how to create a more mature System Development Life Cycle (SDLC) process, which encourages the creation of high-quality computer-based business systems.

Prerequisites:

The following courses are recommended - Introduction to Business Systems Analysis ([TR001](#)), Modeling Essential Business Data ([TR012](#)), Structured Business Systems Design ([TR027](#)) and Business Systems Testing Fundamentals ([TR037](#)) or similar experience.

Description:

This course presents an integrated view of the information presented in the structured SDLC courses (see prerequisites). The Capability Maturing Model for the SDLC is discussed and a view of a mature, modern SDLC is explained. The YSM models for the analysis and design phases of the SDLC are covered and the testing of all deliverables is shown to be integral to producing a high-quality product. The various types of SDLC productivity tools are covered and their functions explained. Hands-on workshops using a JAD format are used to reinforce concepts.

Objectives:

Upon successful completion of this course, the student should be able to:

- Describe the philosophy behind the current industry approach to a mature SDLC process
- Identify the correct sequence of models and deliverables in the current YSM SDLC
- Create and test logical and physical data flow diagrams, mini-specs, event tables and entity-relationship diagrams to create the physical and logical models that are the major deliverables of the YSM SDLC
- Choose the SDLC productivity tools that are critical to a high-quality approach to the YSM SDLC
- Identify the system requirements and constraints that must be defined when selecting a system architecture.